QSFP28-100GBase-DAC-CUxxM-FL Quick Spec:

Part Number:	QSFP28-100GBase-DAC-CU1M-FL QSFP28-100GBase-DAC-CU2M-FL QSFP28-100GBase-DAC-CU3M-FL QSFP28-100GBase-DAC-CU4M-FL QSFP28-100GBase-DAC-CU5M-FL
Form Factor:	QSFP28-QSFP28
Cable Type:	Twinax (DAC)
Rate Category:	100GBase-100GBase
Length:	1m, 2m, 3m, 4m, 5m
Active/Passive:	Passive

QSFP28-100GBase-DAC-CUxxM-FL Features

- Up to 100 GBd bi-directional data links
- Compliant with QSFP28 MSA specifications
- Fully Compliant with IEEE802.3bj specifications
- AC coupled inputs and outputs
- 100 Ohm differential impedance
- All-metal housing for superior EMI performance
- Single power supply 3.3V, low power consumption
- RoHS Compliance
- Operating case temperature range: 0 to 70 deg C

QSFP28-100GBase-DAC-CUxxM-FL Applications

- 100G Ethernet
- High performance computing interconnect

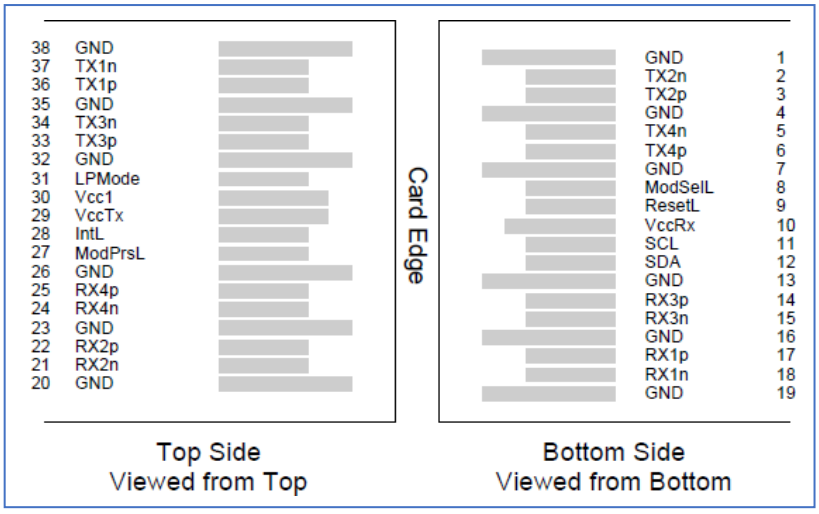
Product Description

The FluxLight's QSFP28-100GBase-DAC-CUxxM-FL is suitable for very short distance and offer a highly cost-effective way to establish a 100-Gigabit link between QSFP28 ports. QSFP28 are designed for a high-density cabling interconnect system capable of delivering an aggregate data bandwidth of 100Gbps. This interconnect system is fully compliant with QSFP28 MSA. The QSFP28 cables support the bandwidth transmission requirements as defined by IEEE802.3bj(100Gbps) .

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Differential input impedance	Zin	90	100	110	ohm	
Operating Case Temperature	Topc	0		70	degC	
Storage Temperature	Tst	-40		85	degC	
Relative Humidity (non-condensation)	RS	-		85	%	
Supply Voltage	VCC3	3.135		3.465	V	
Voltage on LVTTL Input	Vi lvttl	-0.3		VCC3+0.2	V	

Pin Assignment and Description

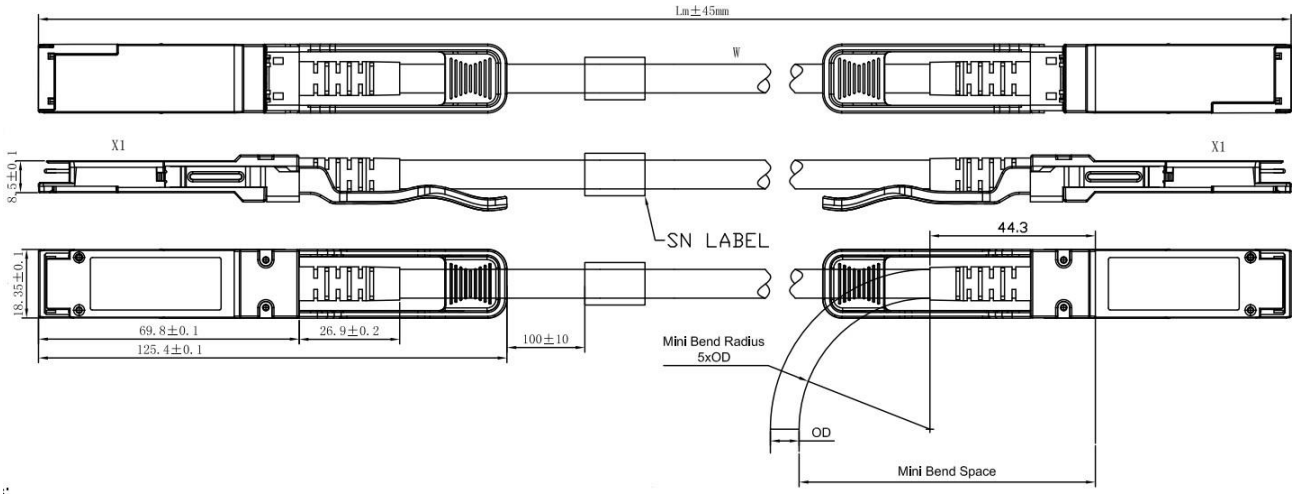


Pin Assignment

Pin	Logic	Symbol	Description	Plug Sequence
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3
7		GND	Ground	1
8	LVTTL-I	ModSelL	Module Select	3
9	LVTTL-I	ResetL	Module Reset	3
10		Vcc Rx	+3.3V Power Supply Receiver	2
11	LVCMOS-I/O	SCL	2-wire serial interface clock	3
12	LVCMOS-I/O	SDA	2-wire serial interface data	3
13		GND	Ground	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3
15	CML-O	Rx3n	Receiver Inverted Data Output	3
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3
18	CML-O	Rx1n	Receiver Inverted Data Output	3
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	3
28	LVTTL-O	IntL	Interrupt	3
29		Vcc Tx	+3.3V Power supply transmitter	2
30		Vcc1	+3.3V Power supply	2
31	LVTTL-I	LPMode	Low Power Mode	3
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3
34	CML-I	Tx3n	Transmitter Inverted Data Input	3
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3
37	CML-I	Tx1n	Transmitter Inverted Data Input	3
38		GND	Ground	1

Mechanical Dimensions

Units: nm



ESD

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all other electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Laser Safety

This is a Class 1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).